

NEAR PATH LOGIC

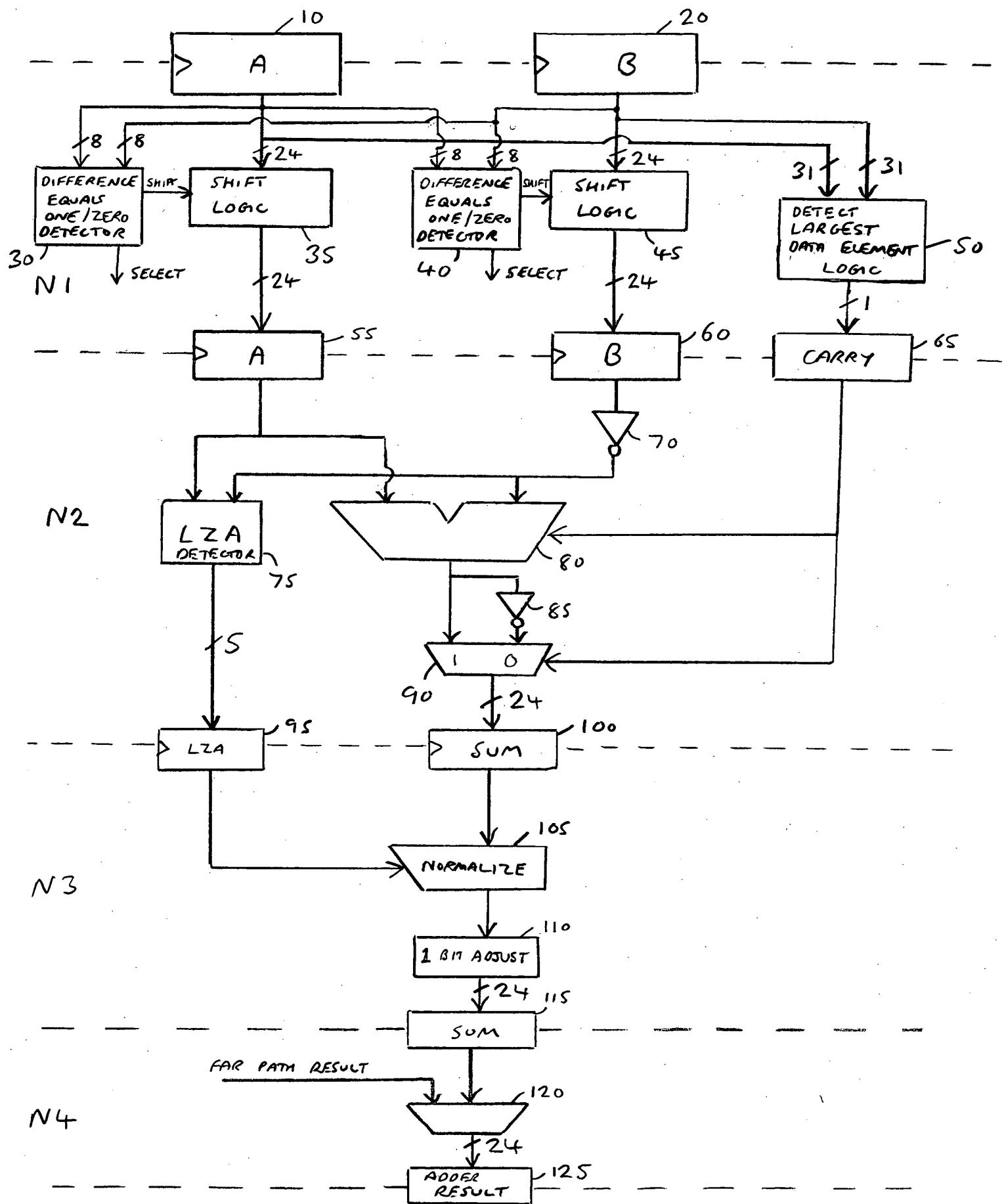


FIG. 1

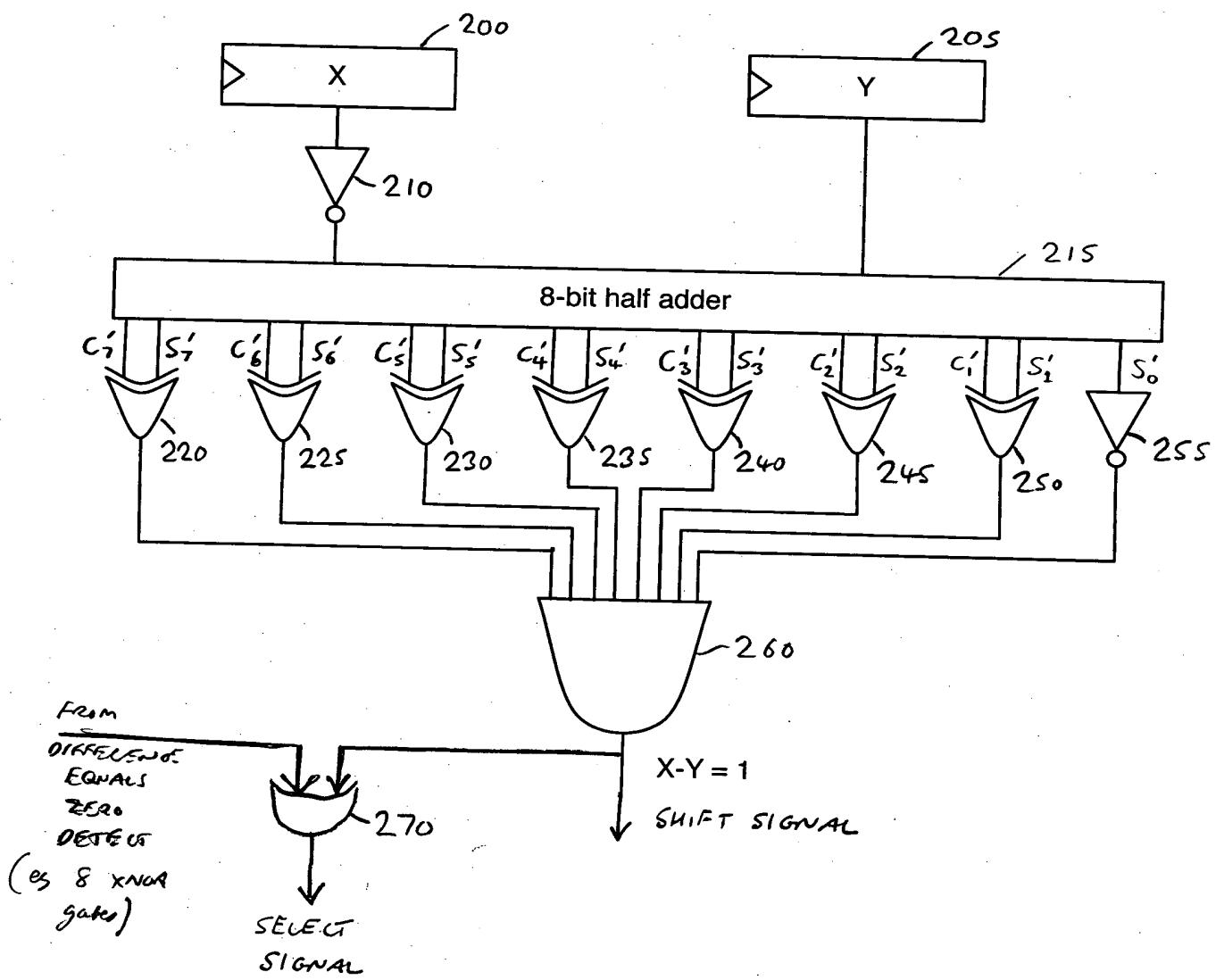


FIG. 2

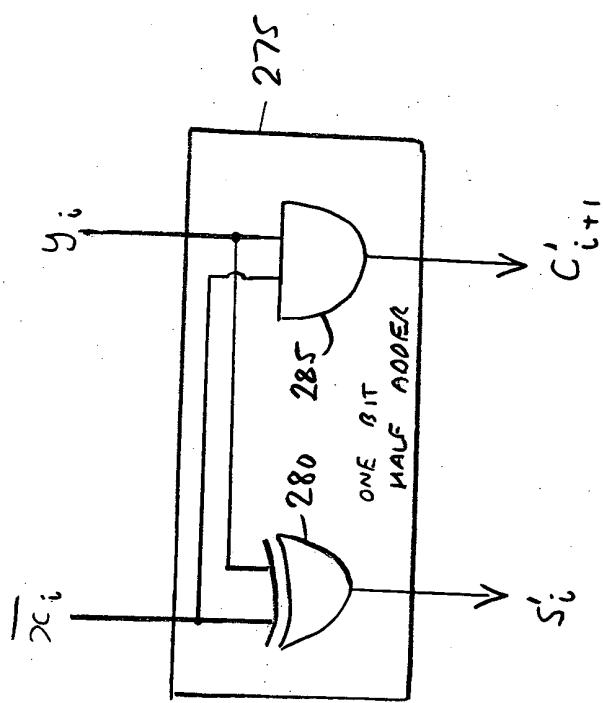


FIG. 3

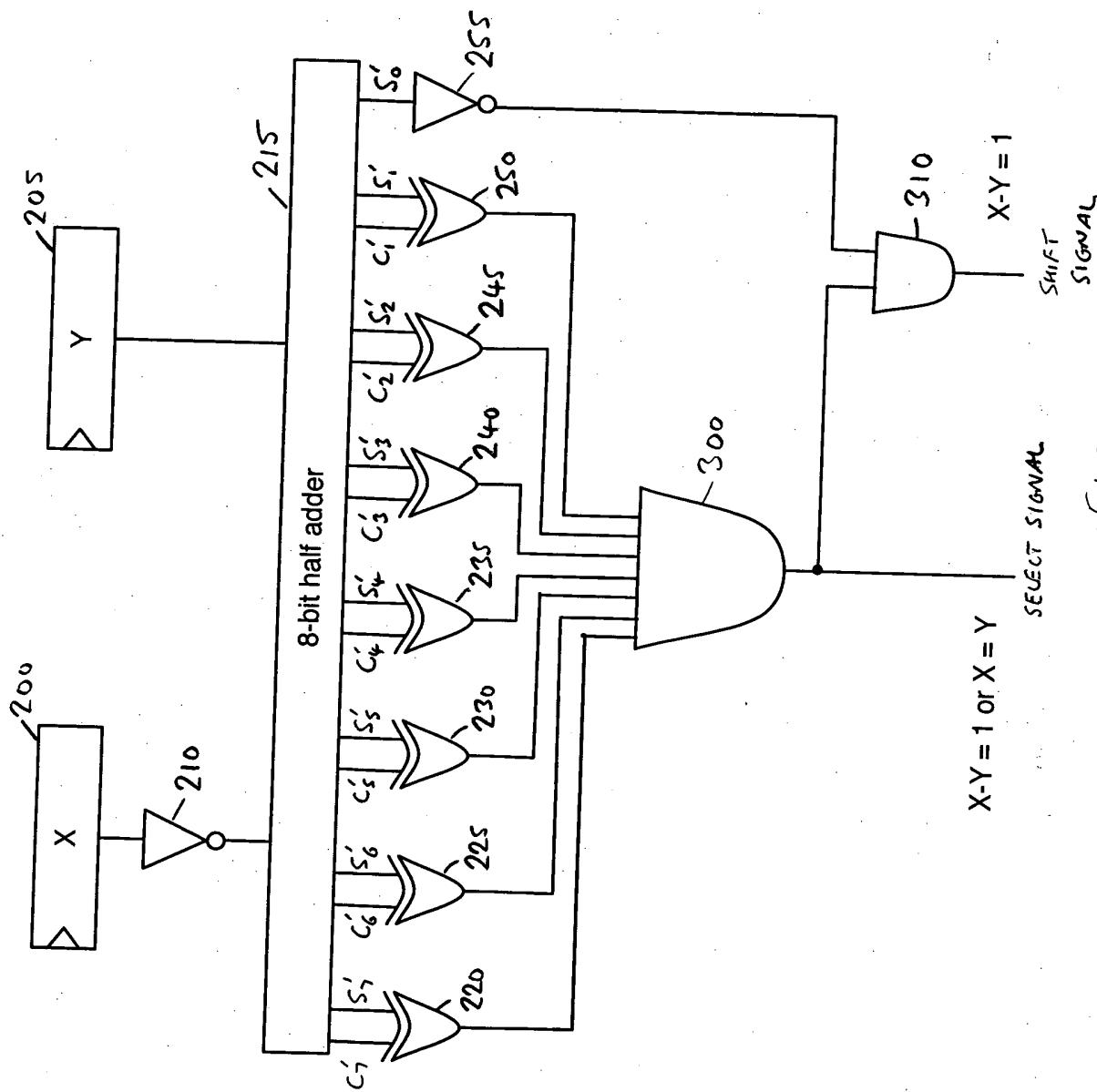


FIG. 4

PROCESS PERFORMED
IN EACH
DETECTOR LOGIC
UNIT

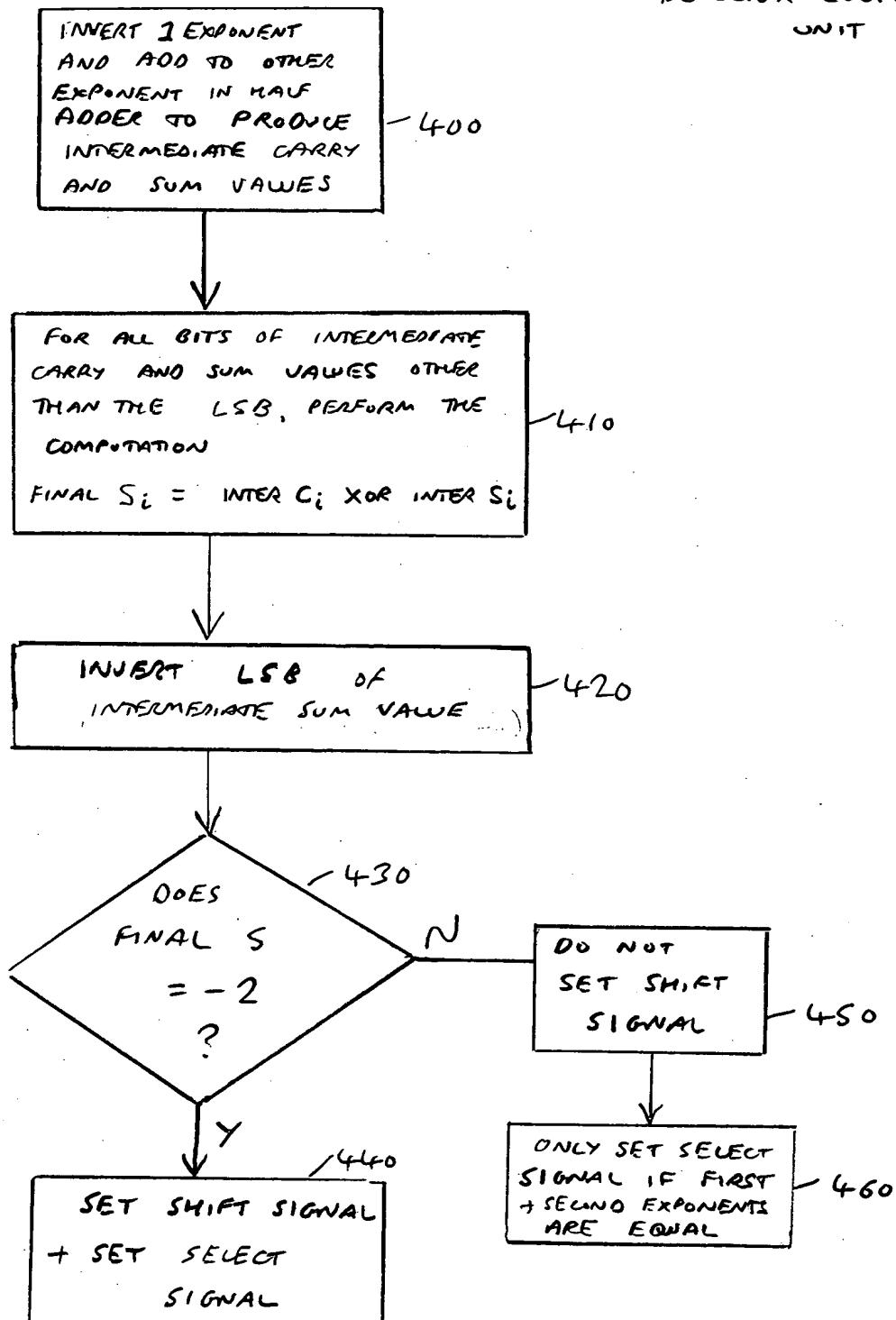


FIG. 5

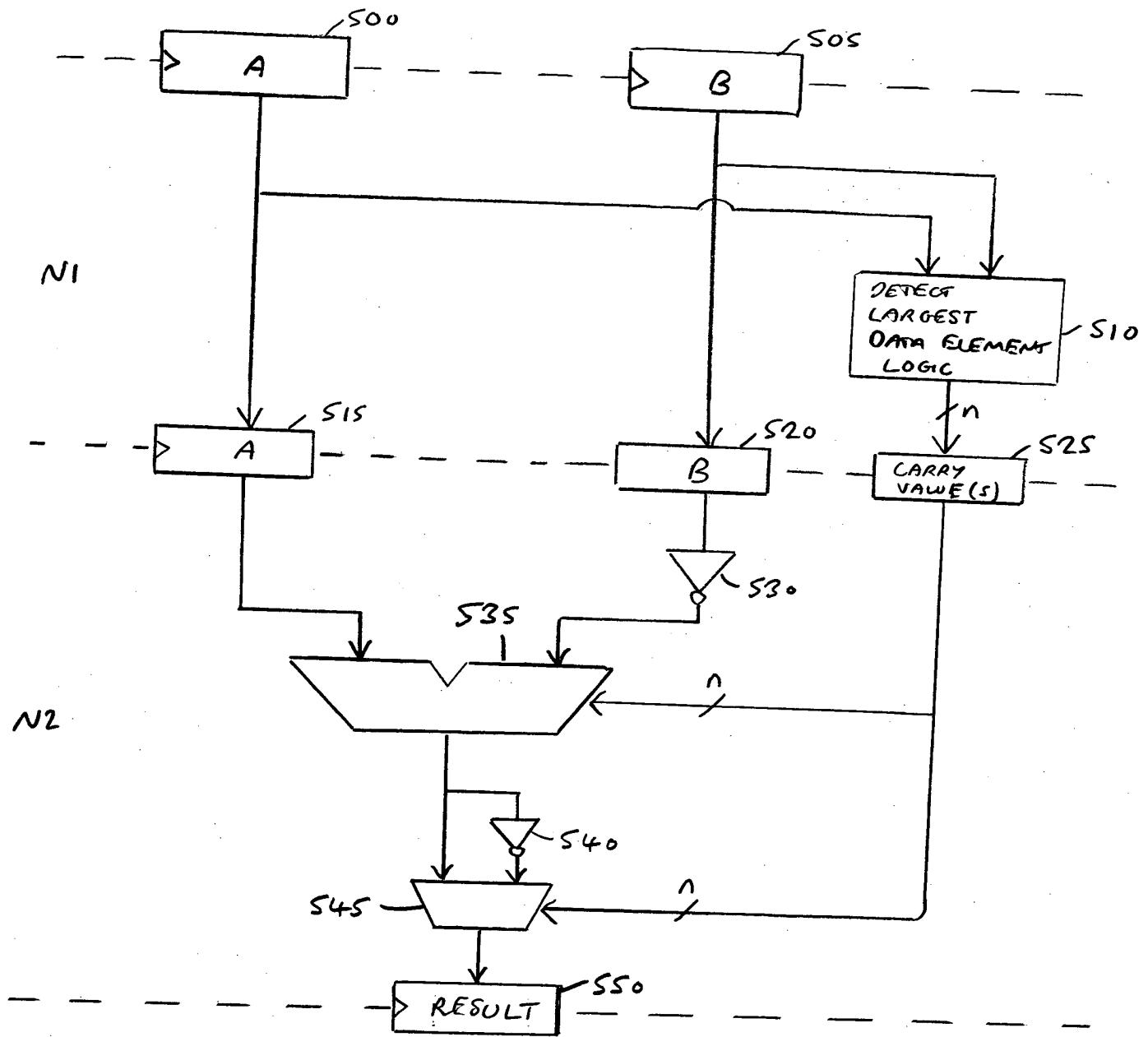


FIG. 6

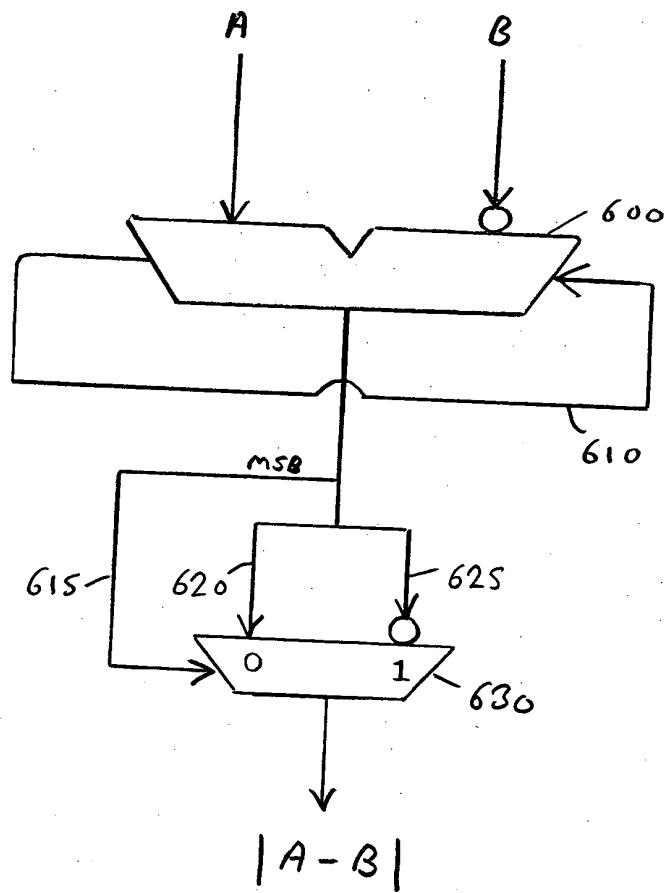


FIG. 7 (PRIOR ART)

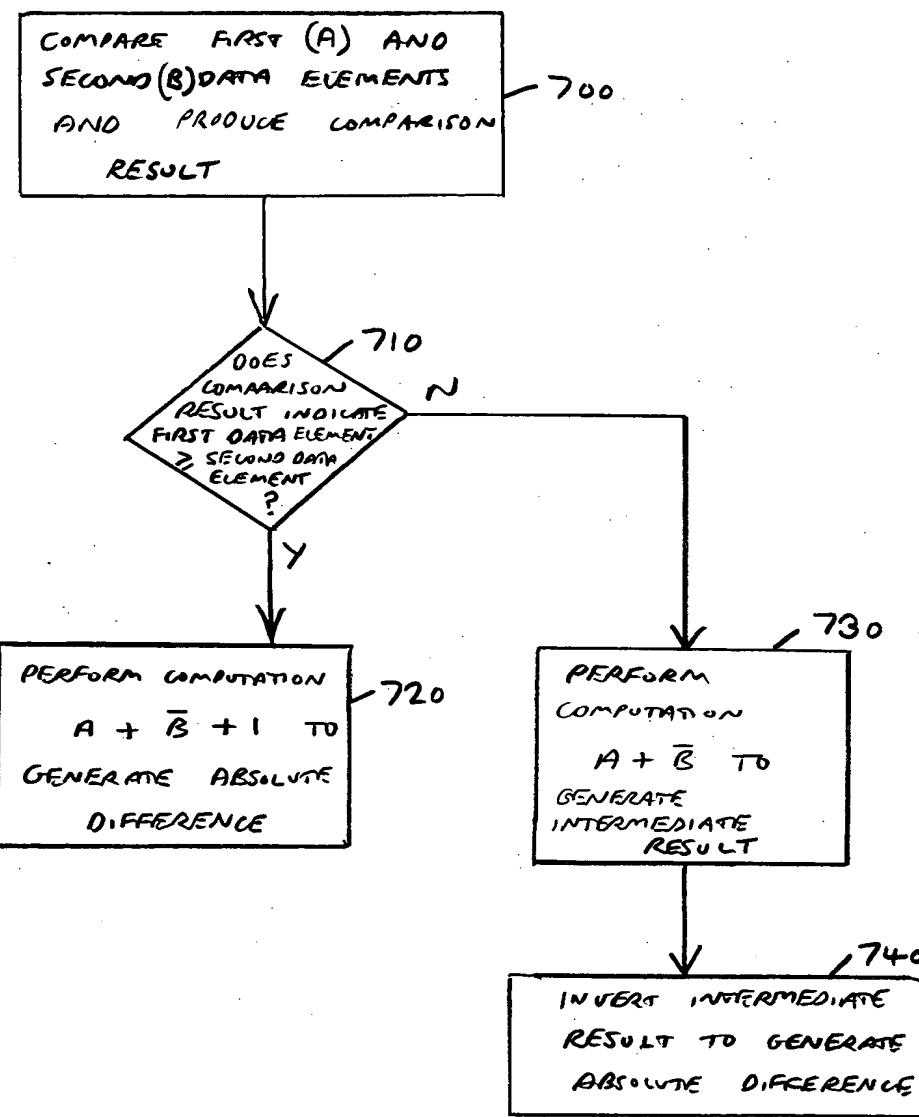


FIG. 8